



156	233	
Class	Subclass	
ISSUE CLASSIFICATION		

Tr. 490 U. S. 810

PATENT NUMBER  
**6338767**

**U.S. UTILITY Patent Application**

O.I.P.E. 1089	PATENT DATE JAN 15 2002
SCANNED Q.A.	

CLASS 319	SUBCLASS	ART UNIT 2816	EXAMINER
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T	APPLICATION NO. 09/484899	CONT/PRIOR YES	CLASS 327	SUBCLASS 233	ART UNIT 2816	EXAMINER Mages
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Seiichi Nakatani  
Kouichi Hirano

A. Circuit component built-in module and method for producing the same

TITLE

PTC-2040  
12/99

**ISSUING CLASSIFICATION**

12/12/01 Formal Drawn at 6:00 PM 1/18/00

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	6	6	1	22	1
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	CURTIS MAYES PRIMARY EXAMINER			NOTICE OF ALLOWANCE MAILED <i>6/21/01</i>	
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	<i>H. Mayes</i> (Primary Examiner)	6/19/01 (Date)	<i>Nansley</i> (Legal Instruments Examiner)	\$1240.00 (Date) <i>6-22-01</i>	Amount Due Date Paid <i>9-21-01</i>
				ISSUE BATCH NUMBER <i>1-09</i>	

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Form PTO-436A  
(Rev. 6/99)

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**ISSUE FEE IN FILE**

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**IN FILE** (Attached in pocket on right inside flap)

Formal Drawings (       shis) set

(FACE)